

Decomposition - oriented synthesis of finite state machines

Mjakosin, Pjotr; Sokolov, M.; **Udre, Jüri** Proceedings of the 2nd Conference "Mixed Design of VLSI Circuits", Lodz, 1995 1995 / p. 229-234

Synthesis of hierarchically testable FSM networks

Kasirova, Lilia; **Keevallik, Andres**; **Kruus, Margus** Proceedings of the 2nd Conference "Mixed Design of VLSI Circuits", Lodz, 1995 1995 / p. 253-259