

**A generic synthesizable NoC switch with a scalable testbench**

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**A new high power factor single-phase diode rectifier with optimum ripple-power conversion**

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**A tool for advanced learning of LFSR-based testing principles**

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**Automated XML-based test modelling for mixed-signal circuits**

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**A bio-impedance signal synthesiser (BISS) for testing of an adaptive filtering system**

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**Bus bar test bench development for common 3x3 matrix converter**

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**Clamp mode package diffusion welded power SiC Schottky diodes**

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**Development of automated measurement setup for standard resistors**

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**Establishment of national standard of voltage unit in Estonia**

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**Functional test generation for finite state machines**

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**Graph embedding in boolean hypercube**

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**Improved VHDL input for high-level synthesis tool xTractor**

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**Isolated DC/DC converter topology with a three-phase intermediate AC-link**

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**LFSR polynomial and seed selection using genetic algorithm**

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**Multifrequency vector impedance measurement: undersampled DFT with noise pre-filtering**

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**SiC Schottky diode for use in power convertors**

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