

At-speed self-testing of high-performance pipe-lined processing architectures [Electronic resource]
Gorev, Maksim; Ubar, Raimund-Johannes; Ellerjee, Peeter; Devadze, Sergei; Raik, Jaan; Min, Mart 31st Norchip Conference : Vilnius, Lithuania, 11-12 November 2013 : conference program and papers 2013 / p. 1-6 : ill [USB]

At-speed testing and test quality evaluation for high-performance pipelined systems Töökiiruse testimine ja testi kvaliteedi hindamine körgjöudlus-konveierarhitektuuriga süsteemidele
Gorev, Maksim 2015 <https://digi.lib.ttu.ee/i/?3953>

A benchmark suite for evaluating the efficiency of test tools

Kruus, Helena; Ubar, Raimund-Johannes; Ellerjee, Peeter; Gorev, Maksim; Pesonen, Vadim; Devadze, Sergei; Orasson, Elmet; Brik, Marina; Min, Mart; Annus, Paul; Kruus, Margus; Meigas, Kalju BEC 2012 : 2012 13th Biennial Baltic Electronics Conference : proceedings of the 13th Biennial Baltic Electronics Conference : October 3-5, 2012, Tallinn, Estonia 2012 / p. 85-88 : ill

Combinational fault simulation in sequential circuits

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EEG analyzer prototype based on FPGA

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Fault simulation with parallel exact critical path tracing in multiple core environment

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FPGA implementation of the polynomial curve fitting

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FPGA-based implementation of EEG analyzer

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FPGA-based implementation of EEG analyzer for detection of depressive disorder

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3D parallel fault simulation

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