

Automated minimization of concurrent online checkers for network-on-chips

Saltarelli, Pietro; Niazmand, Behrad; Hariharan, Ranganathan; Raik, Jaan; Jervan, Gert; Hollstein, Thomas 10th International Symposium on Reconfigurable and Communication-centric Systems-on-Chip (ReCoSoC 2015) : Bremen, 29 June - 1 July 2015 2015 / [8] p. : ill <http://dx.doi.org/10.1109/ReCoSoC.2015.7238079>

AWAIT : an ultra-lightweight soft-error mitigation mechanism for network-on-chip links

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Comprehensive performance and robustness analysis of 2D turn models for network-on-chips

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Samman, Faizal; Hollstein, Thomas International journal of innovative computing, information and control 2019 / p. 305-319 : ill <https://doi.org/10.24507/ijicic.15.01.305> <http://www.ijicic.net/> <http://www.ijicic.net/servlet/Download?contentID=CONT5b67c7cbc23142feade914ceeabdbf4d> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

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QoSInNoC: analysis of QoS-aware NoC architectures for mixed-criticality applications

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