

Design and verification of secure cache wrapper against access-driven side-channel attacks

Niazmand, Behrad; Azad, Siavoosh Payandeh; Jervan, Gert; Sepulveda, Johanna Euromicro Conference on Digital System

Design : DSD 2019 : 28 - 30 August 2019 Kallithea, Chalkidiki, Greece : proceedings 2019 / p. 672-676 : ill

<https://doi.org/10.1109/DSD.2019.00108>

Fast and efficient cache behaviour prediction

Ferdinand, Christian; Wilhelm, Reinhard Proceedings of the Estonian Academy of Sciences. Engineering 1998 / 2, p. 69-88: ill

LiD-CAT: A lightweight detector for cache ATtacks

Reinbrecht, Cezar; Hamdioui, Said; Taouil, Mottaqiallah; **Niazmand, Behrad; Ghasempouri, Tara; Raik, Jaan;** Sepulveda,

Johanna 2020 IEEE European Test Symposium (ETS) : ETS 2020, May 25-29, 2020 Tallinn, Estonia : proceedings 2020 / 6 p. : ill

<https://doi.org/10.1109/ETS48528.2020.9131603>

Mikroprotsessorsüsteemid : kursuseprojekti metoodiline juhend

Toomsalu, Arvo 1996 https://www.ester.ee/record=b1619347*est

Towards formal verification of cache access-based side-channel attacks

Niazmand, Behrad; Reinbrecht, Cezar; **Raik, Jaan; Jervan, Gert;** Sepulveda, Johanna Testmethoden und Zuverlässigkeit von

Schaltungen und Systemen, TUZ 2019 2019 / 2 p. : tab <http://www.informatik.uni-bremen.de/tuz/2019>