

A model for the superconducting microstrip line

Costa, Luis R.J.; Linna, Riku; Seppä, Heikki; **Valtonen, Martti** BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 439-442: ill

An implementation of simulated annealing for analog circuit optimization

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Asynchronous start-up of a synchronous motor

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Genetic optimization in circuit simulation

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Overview of recent steady state analysis methods

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Piecewise-linear DC analysis of nonlinear circuits

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Pulse generation in a superconducting microstrip line

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Self-generating lookup table models combined with DC analysis

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