

**FPGA technological mapping for low power implementation**

**Kasirova, Lilia; Evartson, Teet;** Tveretina, Olga Proceedings of the 4th International Workshop Mixed Design of Integrated Circuits and Systems : MIXDES'97 : Poznan, Poland, 12-14 June 1997 1997 / p. 259-264

**State assignment for CMOS implementation of low power FSM networks**

**Zaugarov, Viktor; Kasirova, Lilia; Tveretina, Olga** Mixed Design of Integrated Circuits and Systems : education of computer aided design of modern ICs and devices : 3rd Advanced Training Course, Lodz, Poland, 30 May - 1 June 1996 1996 / p. 195-198: ill