

**AWAIT : an ultra-lightweight soft-error mitigation mechanism for network-on-chip links**

Janson, Karl; Pihlak, Rene; Azad, Siavoosh Payandeh; Niazmand, Behrad; Jervan, Gert; Raik, Jaan 2018 13th International Symposium on Reconfigurable Communication-centric Systems-on-Chip (ReCoSoC), Lille, France, July 9th-11th, 2018 / p. 1-6 : ill <https://doi.org/10.1109/ReCoSoC.2018.8449374>

**Comprehensive performance and robustness analysis of 2D turn models for network-on-chips**

Azad, Siavoosh Payandeh; Niazmand, Behrad; Janson, Karl; Kogge, Thilo; Raik, Jaan; Jervan, Gert; Hollstein, Thomas 2017 IEEE International Symposium on Circuits and Systems (ISCAS) 2017 / p. 1476-1479 : ill <https://doi.org/10.1109/ISCAS.2017.8050634>

**Fault-aware performance assessment approach for embedded networks**

Malburg, Jan; Janson, Karl; Raik, Jaan; Dannemann, Frank 2019 22nd International Symposium on Design and Diagnostics of Electronic Circuits & Systems (DDECS), Cluj-Napoca, Romania : proceedings 2019 / 4 p. : ill <https://doi.org/10.1109/DDECS.2019.8724670>

**From online fault detection to fault management in network-on-chips : a ground-up approach**

Azad, Siavoosh Payandeh; Niazmand, Behrad; Janson, Karl; Nevin, George; Oyeniran, Adeboye Stephen; Putkaradze, Tsotne; Apneet Kaur; Raik, Jaan; Jervan, Gert; Ubar, Raimund-Johannes; Hollstein, Thomas Proceedings 2017 IEEE 20th International Symposium on Design and Diagnostics of Electronic Circuit & Systems(DDECS) : April 19-21, 2017, Dresden, Germany 2017 / p. 48-53 : ill <https://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=7934553>

**Handling of SETs on NoC links by exploitation of inherent redundancy in circular input buffers [Online resource]**

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**Software-level TMR approach for on-board data processing in space applications**

Janson, Karl; Treudler, Carl Johann; Hollstein, Thomas; Raik, Jaan; Jenihhin, Maksim; Fey, Goerschwin 21st IEEE International Symposium on Design and Diagnostics of Electronic Circuits and Systems : DDECS 2018 : Budapest, Hungary 25-27 April, 2018 : proceedings 2018 / p. 147-152 : ill <https://doi.org/10.1109/DDECS.2018.00033>

**Techniques for robust routing, communication and computation in multiprocessor systems = Robustse marsruutimise, side ja arvutuse tehnikad mitneprotsessorilistes süsteemides**

Janson, Karl 2021 [https://www.ester.ee/record=b5396084\\*est](https://www.ester.ee/record=b5396084*est) <https://digikogu.taltech.ee/et/Item/c9091d5c-dcd8-4b21-95a7-84ead85241e6> <https://doi.org/10.23658/taltech.3/2021>