

At-speed testing and test quality evaluation for high-performance pipelined systems Töökiirusele testimine ja testi kvaliteedi hindamine körgjöudlus-konveierarhitektuuriga süsteemidele
Gorev, Maksim 2015 <https://digi.lib.ttu.ee/i/?3953>

Digital test in WEB-based environment
Ivask, Eero 2006 https://www.esther.ee/record=b2158119*est

Fault simulation of digital systems = Digitaalsüsteemide rikete simuleerimine
Devadze, Sergei 2009 <https://digi.lib.ttu.ee/i/?445> https://www.esther.ee/record=b2508727*est

Formal verification and error correction on high-level decision diagrams = Formaalne verifitseerimine ja vigade parandamine körgtasemelistel otsustusdiagrammidel
Karputkin, Anton 2012

Hierarchical test generation for digital circuits represented by Decision Diagrams : thesis on informatics and system engineering
Raik, Jaan 2001 https://www.esther.ee/record=b1578107*est

High-level implementation-independent software-based self-test for RISC type microprocessors = Mikroprotsessorite tarkvarapõhine implementatsioonist mittesõltuv funktsionaalne enesekontroll
Oyeniran, Adeboye Stephen 2020 <https://digikogu.taltech.ee/et/item/08a75fb-3f71-4fe4-b3d0-3f37a9a5f36d>

Hybrid built-in self-test : methods and tools for analysis and optimization of BIST = Sisseehitatud hübridne isetestimine : meetodid ja vahendid analüüsiks ning optimeerimiseks
Orasson, Elmet 2007 https://www.esther.ee/record=b2305436*est

Investigation and development of test generation methods for control part of digital systems
Brik, Marina 2002 http://www.esther.ee/record=b1688656*est

Methods for improving the accuracy and efficiency of fault simulation in digital systems = Meetodid digitaalsüsteemide rikete simuleerimise täpsuse ja efektiivsuse tõstmiseks
Kõusaar, Jaak 2019 <https://digi.lib.ttu.ee/i/?11667>

Optimization of built-in self-test in digital systems = Sisseehitatud enesetestimise optimeerimine digitaalsüsteemides
Kruus, Helena 2011

Selected issues of modeling, verification and testing of digital systems
Jutman, Artur 2004 https://www.esther.ee/record=b1989760*est

Self-diagnosis in digital systems = Isediagnoosivad digitaalsüsteemid
Kostin, Sergei 2012 https://www.esther.ee/record=b2757857*est

Simulation-based hardware verification with high-level decision diagrams = Simuleerimisel põhinev riistvara verifitseerimine körgtaseme otsustusdiagrammidel
Jenihhin, Maksim 2008 https://www.esther.ee/record=b2431332*est

Software-based self-test for microprocessors with high-level decision diagrams = Mikroprotsessorite tarkvara-põhine enesetestimine körgtasandi otsustusdiagrammide põhjal
Jasnetski, Artjom 2018 <https://digi.lib.ttu.ee/i/?10629>

System modeling for processor-centric test automation = Süsteemide modelleerimine protsessorikesksete testprogrammide sünteesi automatiseerimiseks
Tšertov, Anton 2012 https://www.esther.ee/record=b2751131*est

Исследование и разработка методов анализа диагностических тестов для цифровых схем : автореферат ... кандидата технических наук (05.13.01)
Kitsnik, Peeter 1981 https://www.esther.ee/record=b1337813*est

Исследование и разработка методов анализа диагностических тестов для цифровых схем : диссертация на соискание ученой степени кандидата технических наук (05.13.01)
Kitsnik, Peeter 1980 https://www.esther.ee/record=b4632972*est

Исследование и разработка методов управления поиском дефектов в цифровых схемах : автореферат кандидата технических наук (05.13.01)

Egartson, Teet 1986 https://www.ester.ee/record=b1301665*est