

Fast iterative circuits and RAM-based mergers to accelerate data sort in software/hardware systems

Sklyarov, Valery; Skliarova, Iuliia; **Rjabov, Artjom; Sudnitsõn, Aleksander** Proceedings of the Estonian Academy of Sciences 2017 / p. 323-335 : ill <https://doi.org/10.3176/proc.2017.3.07> http://www.eester.ee/record=b2355998*est [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

FPGA-based accelerators for parallel data sort

Sklyarov, Valery; Skliarova, Iuliia; **Sudnitsõn, Aleksander** Applied computer systems 2014 / p. 53-63 : ill

High-performance information processing in distributed computing systems

Sklyarov, Valery; **Rjabov, Artjom**; Skliarova, Iuliia; **Sudnitsõn, Aleksander** International journal of innovative computing, information and control 2016 / p. 139-160 : ill https://www.researchgate.net/publication/297047447_High-performance_information_processing_in_distributed_computing_systems [Journal metrics at Scopus](#) [Article at Scopus](#)