

Design and verification of Cyber-Physical Systems using TrueTime, evolutionary optimization and UPPAAL
Balasubramaniyan, Sreram; Srinivasan, Seshadhri; Buonopane, Furio; Balasubramanian, Subathra; **Vain, Jüri**; Ramaswamy, Sriniv
Microprocessors and Microsystems 2016 / p. 37-48 : ill <http://dx.doi.org/10.1016/j.micpro.2015.12.006>

Model checking response times in Networked Automation Systems using jitter bounds
Srinivasan, Seshadhri; Buonopane, Furio; **Vain, Jüri**; Ramaswamy, Sriniv
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