

Design and verification of Cyber-Physical Systems using TrueTime, evolutionary optimization and UPPAAL

Balasubramaniyan, Sreram; Srinivasan, Seshadhri; Buonopane, Furio; Balasubramanian, Subathra; **Vain, Jüri**; Ramaswamy, Srin
Microprocessors and microsystems 2016 / p. 37-48 : ill <https://doi.org/10.1016/j.micpro.2015.12.006> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Integration of iUML-B and UPPAAL timed automata for development of real-time systems with concurrent processes

Shokri-Manninen, Fatima; **Tsiopoulos, Leonidas**; **Vain, Jüri**; Walden, Marina Rigorous State-Based Methods 7th International Conference, ABZ 2020, Ulm, Germany, May 27–29, 2020 : proceedings 2020 / p. 186-202 https://doi.org/10.1007/978-3-030-48077-6_13 [Conference proceedings at Scopus](#) [Article at Scopus](#)

Model checking response times in Networked Automation Systems using jitter bounds

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Provably correct aspect-oriented modeling with UPPAAL timed automata

Vain, Jüri; **Tsiopoulos, Leonidas**; **Kanter, Gerd** System assurances : modeling and management 2022 / p. 447-476
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