

Computing sorted subsets for data processing in communicating software/hardware control systems
Sklyarov, Valery; Skliarova, Ioulia; **Rjabov, Artjom; Sudnitsõn, Aleksander** International journal of computers communications & control 2016 / p. 126-141 : ill <http://dx.doi.org/10.15837/ijccc.2016.1.1442>

Fast processing of non-repeated values in hardware

Skliarova, Ioulia; Sklyarov, Valery; **Sudnitsõn, Aleksander** Elektronika ir elektrotehnika = Electronics and electrical engineering 2017 / p. 74-77 : ill <http://dx.doi.org/10.5755/j01.eie.23.3.18336>

Hardware-based systems for partial sorting of streaming data

Rjabov, Artjom BEC 2016 : 2016 15th Biennial Baltic Electronics Conference : proceedings of the 15th Biennial Baltic Electronics Conference : Tallinn University of Technology, October 3-5, 2016, Tallinn, Estonia 2016 / p. 59-62 : ill
http://www.estet.ee/record=b2150914*est

Processing sorted subsets in a multi-level reconfigurable computing system

Rjabov, Artjom; Sklyarov, Valery; Skliarova, Ioulia; **Sudnitsõn, Aleksander** Elektronika ir elektrotehnika = Electronics and electrical engineering 2015 / p. 30-33 : ill <http://dx.doi.org/10.5755/j01.eee.21.2.11509>

RAM-based mergers for data sort and frequent item computation [Electronic resource]

Rjabov, Artjom; Sklyarov, Valery; Skliarova, Ioulia; **Sudnitsõn, Aleksander** 2017 40th International Convention on Information and Communication Technology, Electronics and Microelectronics (MIPRO), May 22 - 26, 2017, Opatija, Croatia : proceedings 2017 / p. 176-181 : ill. [CD-ROM] <http://dx.doi.org/10.23919/MIPRO.2017.7973413>

Zynq-based system for extracting sorted subsets from large data sets

Sklyarov, Valery; Skliarova, Ioulia; **Rjabov, Artjom; Sudnitsõn, Aleksander** Journal of microelectronics, electronic components and materials 2015 / p. 142-152 : ill