

**Area efficient hexadecimal divider circuit implementation based on USP-awadhoot division algorithm**

**Patankar, Udayan Sunil; Koel, Ants;** Patankar, Sunil M.; Flores, Miguel E. IEEE International Conference on Engineering, Technology and Innovation (ICE/ITMC) 2021 / p. 1-8 <https://doi.org/10.1109/ICE/ITMC52061.2021.9570263>

**Complex division by Baudhayan triplet algorithm using novel state of the art USP-awadhoot divider**

**Patankar, Udayan Sunil; Koel, Ants;** Patankar, Sunil M.; Flores, Miguel E. IEEE 4th Advanced Information Management, Communicates, Electronic and Automation Control Conference (IMCEC) 2021 / p. 1918-1922  
<https://doi.org/10.1109/IMCEC51613.2021.9482010>

**Divider implementation based on USP-awadhoot division algorithm For area optimization**

**Patankar, Udayan Sunil; Koel, Ants;** Patankar, Sunil M.; Flores, Miguel E. IEEE 4th Advanced Information Management, Communicates, Electronic and Automation Control Conference (IMCEC) 2021 / p. 1675-1682  
<https://doi.org/10.1109/IMCEC51613.2021.9482132>

**Elements of vedic mathematics**

**Patankar, Udayan Sunil;** Patankar, Sunil M. 2018 [http://www.ester.ee/record=b4764489\\*est](http://www.ester.ee/record=b4764489*est)