

An area aware accelerator for elliptic curve point multiplication

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Areeba : an area efficient binary huff-curve architecture

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Design space exploration of SABER in 65nm ASIC

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Elliptic-curve crypto processor for RFID applications

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Evaluating NTT/INTT implementation styles for post-quantum cryptography

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A 4-Stage pipelined architecture for point multiplication of binary huff curves

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High-speed design of postquantum cryptography with optimized hashing and multiplication

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Multiplierless design of high-speed very large constant multiplications

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Multiplierless design of very large constant multiplications in cryptography

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An open-source library of large integer polynomial multipliers

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A versatile and flexible multiplier generator for Large integer polynomials

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