

Advanced Hardware Protection Mechanisms : A Study on Logic Locking and Circuit Obfuscation Techniques

[Võrguteavik] = Täiustatud riistvara kaitsemehhanismid : uuring loogikalukustamise ja hägustamise tehnikate kohta

Costa de Almeida, Antonio Felipe 2025 https://www.ester.ee/record=b5752171*est <https://digikogu.taltech.ee/et/Item/9840306f-39a2-4ac7-b275-9a841a75857b> <https://doi.org/10.23658/taltech.42/2025>

CAC 2.0 : a corrupt and correct logic locking technique resilient to structural analysis attacks

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CAC 2.0 : a corrupt and correct logic locking technique resilient to structural analysis attacks

Aksoy, Levent; Yasin, Muhammad; Pagliarini, Samuel Nascimento arXiv.org 2024 / 6 p. : ill <https://doi.org/10.48550/arXiv.2401.07142>

Hardware obfuscation of digital FIR filters

Aksoy, Levent; Hepp, Alexander; Baehr, Johanna; Pagliarini, Samuel Nascimento 2022 25th International Symposium on Design and Diagnostics of Electronic Circuits and Systems (DDECS) : Prague, Czech Republic : April 6-8, 2022 : proceedings 2022 / p. 68-73 <https://doi.org/10.48550/arXiv.2202.10022> <https://doi.org/10.1109/DDECS54261.2022.9770141>

High-level intellectual property obfuscation via decoy constants

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KRATT : QBF-assisted removal and structural analysis attack against logic locking

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KRATT : QBF-assisted removal and structural analysis attack against logic locking

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Multiplierless design of high-speed very large constant multiplications

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Multiplierless design of very large constant multiplications in cryptography

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RESAA: A Removal and Structural Analysis Attack Against Compound Logic Locking

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