

Architectural solutions for high-speed data processing demands of CERN LHC detectors with FPGA and high-level synthesis

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Wang, Likai; **De Luca, Francesco**; Janssen, Patrick; Bui, Do Phuong Tung; Chen, Kian Wee; Yuan, Chao Journal of building engineering 2025 / art. 114413 <https://doi.org/10.1016/j.jobe.2025.114413>
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