

Design understanding : from logic to specification

Fey, Goerschwin; **Ghasempouri, Tara**; Jacobs, Swen; **Raik, Jaan** Proceedings of the 2018 IFIP/IEEE International Conference on Very Large Scale Integration (VLSI-SoC) : October 8-10, 2018, Verona, Italy 2018 / p. 172–175 : ill <https://doi.org/10.1109/VLSI-SoC.2018.8644732>

Model-based verification with error localization and error correction for C designs

Repinski, Urmaz Программные продукты и системы = Programmye produkty i sistemy = Software & systems 2012 / p. 221-229 : ill