

An implementation of simulated annealing for analog circuit optimization

Starck, Vesa; Valtonen, Martti BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 377-380: ill

Yield optimization of circuits based on simulated annealing and gravity centers methods

Zebbache, Ahmed; Siarry, Patrick BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 371-372: ill