

Area efficient design and implementation of a novel divider circuit block = Uudne efektiivne jagamistehte riistvaraline realiseerimine

Patankar, Udayan Sunil 2025 https://www.eester.ee/record=b5746970*est <https://digikogu.taltech.ee/et/Item/bada7a0d-f54f-48bb-844d-37e23ad8d029> <https://doi.org/10.23658/taltech.29/2025>