

Fast matrix covering in all programmable systems-on-chip

Sklyarov, Valery; Skliarova, Iuliia; **Rjabov, Artjom; Sudnitsõn, Aleksander** Elektronika ir elektrotehnika = Electronics and electrical engineering 2014 / p. 150-153 : ill <https://doi.org/10.5755/j01.eee.20.5.7116> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

FORTUNE: A Negative Memory Overhead Hardware-Agnostic Fault Tolerance Technique in DNNs

Nazari, Samira; **Taheri, Mahdi**; Azarpeyvand, Ali; Afsharchi, Mohsen; **Ghasempouri, Tara**; Herglotz, Christian; **Daneshtalab, Masoud; Jenihhin, Maksim** 2024 IEEE 33rd Asian Test Symposium (ATS) 2024 / p. 1-6 : ill <https://doi.org/10.1109/ATS64447.2024.10915463> [Conference proceedings at Scopus](#) [Article at Scopus](#) [Article at WOS](#)

FPGA-based time and cost effective Hamming weight comparators for binary vectors

Sklyarov, Valery; Skliarova, Iuliia; **Sudnitsõn, Aleksander; Kruus, Margus** Proceedings : EUROCON 2015 : Salamanca, Spain, 8th-11th September 2015 / p. 328-333 : ill <http://dx.doi.org/10.1109/EUROCON.2015.7313700>

G-GPU : a fully-automated generator of GPU-like ASIC accelerators

Perez, Tiago Diadami; Gonçalves, Marcio M.; Gobatto, Leonardo; Brandalero, Marcelo; Azambuja, Jose Rodrigo; **Pagliarini, Samuel Nascimento** 2022 Design, Automation & Test in Europe Conference & Exhibition (DATE 2022) : proceedings 2022 / p. 544 - 547 <https://doi.org/10.23919/DATE54114.2022.9774758>

Hardware accelerators for information retrieval and data mining

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Solving computationally intensive problems in reconfigurable hardware : a case study

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The cluster computation-based hybrid FEM–analytical model of induction motor for fault diagnostics

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