

FPGA netlist partitioning onto heterogeneous FPGA boards

Zahm, U.; Glesner, M. BEC : Baltic Electronics Conference : proceedings of the 4th Biennial Conference, October 9-14, 1994, Tallinn (Estonia). 2 1994 / p. 397-404: ill https://www.ester.ee/record=b2150914*est

A new concept for a self-learning kit for FPGA design

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