

Decompositional design of testable Decompositional design of testable FSM networks

Keevallik, Andres; Kruus, Margus; Lensen, Harri Proceedings of the 4th International Workshop Mixed Design of Integrated Circuits and Systems : MIXDES'97 : Poznan, Poland, 12-14 June 1997 1997 / p. 431-436