

Cycle-based simulation algorithms for digital systems using high-level decision diagrams

Morawiec, Adam; **Ubar, Raimund-Johannes; Raik, Jaan** Design, Automation and Test in Europe : Conference and Exhibition 2000 : Paris, France, March 27-30, 2000 : proceedings 2000 / p. 743

TOP : an algorithm for three-level optimization of PLDs

Dubrova, E.; **Ellervee, Peeter**; Miller, D.M.; Muzio, J.C. Design, Automation and Test in Europe : Conference and Exhibition 2000 : Paris, France, March 27-30, 2000 : proceedings 2000 / p. 751