

**Decompositional synthesis of control-dominated devices for power minimization**

**Kasirova, Lilia; Keevallik, Andres; Sudnitsõn, Aleksander** Mixed design of integrated circuits and systems : proceedings of the 3rd Advanced Training Course : Mixed Design of Integrated Circuits and Systems - Education of Computer Aided Design of Modern ICs and Devices, MIXDES'96, Lodz, Poland, 30 May-1 June 1996 1996 / p. 189-194: ill

**Synthesis of high-level decision diagrams for functional test pattern generation**

**Ubar, Raimund-Johannes; Raik, Jaan; Karputkin, Anton; Tombak, Mati** Proceedings of the 16th International Conference Mixed Design of Integrated Circuits and Systems MIXDES 2009 : Lodz, Poland, 25-27 June, 2009 2009 / p. 519-524 : ill

**Transparency analysis of first-order Takagi-Sugeno systems**

**Riid, Andri; Isotamm, Raul; Rüstern, Ennu** 10th International Conference on System-Modeling-Control : Zakopane, Poland, May 21-25, 2001 : proceedings. Vol. 2 2001 / p. 165-170 : ill