

Extended block-adaptive Fourier analyser

Ronk, Ants ICCSC'02 : 1st IEEE International Conference on Circuits and Systems for Communications, 26-28 June, 2002, St.Petersburg, Russia : proceedings 2002 / p. 428-431 : ill <https://ieeexplore.ieee.org/document/1029133>

Settling time minimization in PLL frequency synthesizers

Min, Mart; Männama, Vello; Paavle, Toivo ICCSC'02 : 1st IEEE International Conference on Circuits and Systems for Communications, 26-28 June, 2002, St.Petersburg, Russia : proceedings 2002 / p. 366-369 : ill <https://ieeexplore.ieee.org/document/1029117>