

**FPGA based fault emulation of synchronous sequential circuits**

**Ellerjee, Peeter; Raik, Jaan; Tihhomirov, Valentin; Ubar, Raimund-Johannes** Proceedings [of] 22nd NORCHIP Conference : Oslo, Norway, 8-9 November 2004 2004 / p. 59-62 <https://ieeexplore.ieee.org/abstract/document/1423822>

**An improved estimation methodology for hybrid BIST cost calculation**

**Jervan, Gert; Peng, Zebo; Ubar, Raimund-Johannes; Korelina, Olga** Proceedings [of] 22nd NORCHIP Conference : Oslo, Norway, 8-9 November 2004 2004 / p. 297-300 : ill