

Communication modelling and synthesis for NoC-based systems with real-time constraints

Tagel, Mihkel; Ellervee, Peeter; Hollstein, Thomas; Jervan, Gert Proceedings of the 2011 IEEE Symposium on Design and Diagnostics of Electronic Circuits and Systems : April 13-15, 2011, Gottbus, Germany 2011 / p. 237-242 : ill
<https://www.semanticscholar.org/paper/Communication-modelling-and-synthesis-for-NoC-based-Tagel-Ellervee/71f9595d88ed06b63367b87188b218fe6da6bd97>

Defect-oriented modul-level fault diagnosis in digital circuits

Kostin, Sergei; Ubar, Raimund-Johannes; Raik, Jaan Proceedings of the 2011 IEEE Symposium on Design and Diagnostics of Electronic Circuits and Systems : April 13-15, 2011, Gottbus, Germany 2011 / p. 81-86

High-performance hardware accelerators for sorting and managing priorities

Sklyarov, Valery; Skliarova, Iuliia; Mihhailov, Dmitri; Sudnitsõn, Aleksander Proceedings of the 2011 IEEE Symposium on Design and Diagnostics of Electronic Circuits and Systems : April 13-15, 2011, Gottbus, Germany 2011 / p. 313-318 : ill
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Probabilistic equivalence checking based on high-level decision diagrams

Karputkin, Anton; Ubar, Raimund-Johannes; Tombak, Mati; Raik, Jaan Proceedings of the 2011 IEEE Symposium on Design and Diagnostics of Electronic Circuits and Systems : April 13-15, 2011, Gottbus, Germany 2011 / p. 423-428 : ill
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