

Asynchronous fault detection in IEEE P1687 instrument network

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Data-driven cross-layer fault management architecture for sensor networks

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Fault management techniques to enhance the reliability of power electronic converters : an overview

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From online fault detection to fault management in network-on-chips : a ground-up approach

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Health management for self-aware SoCs based on IEEE 1687 infrastructure

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IEEE P1687 JTAG demonstrator on FPGA

Shibin, Konstantin; Aleksejev, Igor; Jutman, Artur; Devadze, Sergei DATE 2012 University Booth : Design Automation and Test in Europe : Dresden, Germany, March 12-16, 2012 2012 / 1 p. : ill
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Invited paper: System-Wide Fault Management based on IEEE P1687 JTAG

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On-chip sensors data collection and analysis for SoC health management

Shibin, Konstantin; Jenihhin, Maksim; Jutman, Artur; Devadze, Sergei; Tsertov, Anton 2023 IEEE International Symposium on Defect and Fault Tolerance in VLSI and Nanotechnology Systems (DFT) 2023 / 6 p <https://doi.org/10.1109/DFT59622.2023.10313562>

On-line fault classification and handling in IEEE1687 based fault management system for complex SoCs

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Systematic review of fault tolerant techniques in underwater sensor networks

Vihman, Lauri; Kruusmaa, Maarja; Raik, Jaan Sensors 2021 / art. 3264 <https://doi.org/10.3390/s21093264> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

System-wide fault management based on IEEE P1687 JTAG

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