

Low-area boundary BIST architecture for mesh-like network-on-chip

Raik, Jaan; Govind, Vineeth Proceedings of the 2012 IEEE 15th International Symposium on Design and Diagnostics of Electronic Circuits & Systems (DDECS) : April 18-20, 2012 Tallinn, Estonia 2012 / p. 95-100 : ill

Multiple stuck-at-fault detection theorem

Ubar, Raimund-Johannes; Kostin, Sergei; Raik, Jaan Proceedings of the 2012 IEEE 15th International Symposium on Design and Diagnostics of Electronic Circuits & Systems (DDECS) : April 18-20, 2012 Tallinn, Estonia 2012 / p. 236-241 : ill

Multisine signal generation method for a bioimpedance measurement device

Gorev, Maksim; Pesonen, Vadim; Ellervee, Peeter Proceedings of the 2012 IEEE 15th International Symposium on Design and Diagnostics of Electronic Circuits & Systems (DDECS) : April 18-20, 2012 Tallinn, Estonia 2012 / p. 111-114 : ill

A simulation framework for 3-dimension networks-on-chip with different vertical channel density configurations

Ying, Haoyuan; Jaiswal, Ashok; Abd El Ghany, Mohamed A; **Hollstein, Thomas**; Hofmann, Klaus Proceedings of the 2012 IEEE 15th International Symposium on Design and Diagnostics of Electronic Circuits & Systems (DDECS) : April 18-20, 2012 Tallinn, Estonia 2012 / p. 83-88 : ill