## Challenges for future system-on-chip design

Hollstein, Thomas; Peng, Zebo; **Ubar, Raimund-Johannes**; Glesner, Manfred Circuit Paradigm in the 21st Century: ECCTD '01: proceedings of the 15th European Conference on Circuit Theory and Design: Helsinki University of Technology, Finland, 28th-31st August 2001. Vol 3 2001 / p. 173-176

## Hardware/software co-design: state-of-the-art and future directions

Glesner, Manfred; Hollstein, Thomas; Gasteier, Michael; Münch, Michael BEC'96: the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia: proceedings 1996 / p. 9-16: ill

## Runtime contention and bandwidth-aware adaptive routing selection strategies for networks-on-chip

Samman, Faizal; **Hollstein, Thomas**; Glesner, Manfred IEEE transactions on parallel and distributed systems 2013 / p. 1411-1421 : ill