

Combinatorial algorithms of discrete mathematics

Zakrevskij, Arkadij; Pottosin, Yu.; Cheremisinova, L. 2008 http://www.ester.ee/record=b2350865*est

Combinatorial calculations in many-dimensional Boolean space

Zakrevskij, Arkadij 2012 http://www.ester.ee/record=b2771638*est

Design of FPGA-based circuits using hierarchical finite state machines

Skliarova, Ioulia; Sklyarov, Valery; **Sudnitsõn, Aleksander** 2012 http://www.ester.ee/record=b2857138*est

Design of logical control devices

Zakrevskij, Arkadij; Pottosin, Yu.; Cheremisinova, L. 2009 https://www.ester.ee/record=b2548174*est

Hardware/software co-design for programmable systems-on-chip

Sklyarov, Valery; Skliarova, Ioulia; Silva, João; Rjabov, Artjom; **Sudnitsõn, Aleksander**; Cardoso, Cláudia 2014 http://www.ester.ee/record=b3087107*est

Optimization in Boolean space

Zakrevskij, Arkadij; Pottosin, Yu.; Cheremisinova, L. 2009 http://www.ester.ee/record=b2461762*est

Parallel processing in FPGA-based digital circuits and systems

Sklyarov, Valery; Skliarova, Ioulia 2013 http://www.ester.ee/record=b2946103*est

Solving large systems of logical equations

Zakrevskij, Arkadij D 2013 http://www.ester.ee/record=b3000573*est