

Modular exponent realization on FPGAs

Põldre, Jüri; Tammemäe, Kalle; Mandre, Marek Field-programmable logic and applications : from FPGAs to computing paradigm : 8th International Workshop, FPL'98, Tallinn, Estonia, August 31-September 3, 1998 : proceedings 1998 / p. 336-347: ill

On statistical topography of self-affine sets = Eneseafinsete hulkade statistilisest topograafiast

Mandre, Marek 2019 <https://digi.lib.ttu.ee/?11232>