

At-speed testing of inter-die connections of 3D-SICs in the presence of shore logic

Shibin, Konstantin; Chickermane, Vivek; Keller, Brion; Papameletis, Christos; Marinissen, Erik Jan 2015 Asian Test Symposium :
ATS 2015 : 22-25 November 2015, Mumbai, Maharashtra, India : proceedings 2015 / p. 79-84 : ill <http://dx.doi.org/10.1109/ATS.2015.21>