

Design and test technology for dependable systems-on-chip

2011 https://www.ester.ee/record=b4467408*est

Proceedings of the 2011 IEEE Symposium on Design and Diagnostics of Electronic Circuits and Systems : April 13-15, 2011, Gottbus, Germany

2011 <https://www.computer.org/csdl/proceedings/ddecs/2011/12OmNvTBBbs>

Proceedings of the 2012 IEEE 15th International Symposium on Design and Diagnostics of Electronic Circuits & Systems (DDECS) : April 18-20, 2012 Tallinn, Estonia

2012 http://www.ester.ee/record=b2777270*est