

Improved biasing error in current mode absolute value circuits

Männama, Vello; Wilson, B. Proceedings of the European Conference on Circuit Theory and Design ECCTD'99 : August 29-September 2, 1999, Stresa, Italy 1999 / p. 131-132

Signal distortion and waveform conversion in a classical PLL

Min, Mart; Männama, Vello; Paavle, Toivo Proceedings of the European Conference on Circuit Theory and Design ECCTD'99 : August 29-September 2, 1999, Stresa, Italy 1999 / p. 127-130