

Efficient TPG for a fast at-speed interconnect BIST [Electronic resource]

Jutman, Artur 7th IEEE Workshop on Design and Diagnostics of Electronic Circuits and Systems : April 18-21, 2004, Stará Lesná, Slovakia : proceedings 2004 / p. 223-226 : ill. [CD-ROM]

Enhancing hierarchical ATPG with a functional fault model for multiplexers [Electronic resource]

Raik, Jaan; Ubar, Raimund-Johannes 7th IEEE Workshop on Design and Diagnostics of Electronic Circuits and Systems : April 18-21, 2004, Stará Lesná, Slovakia : proceedings 2004 / p. 219-222 : ill. [CD-ROM]