

### **Integration of high-level synthesis to the courses on reconfigurable digital systems**

Sklyarov, Valery; Skliarova, Iuliia; **Sudnitsõn, Aleksander; Kruus, Margus** 2015 38th International Convention on Information and Communication Technology, Electronics and Microelectronics (MIPRO) : May 25-29, 2015, Opatija, Croatia : proceedings 2015 / p. 166-171 : ill <http://dx.doi.org/10.1109/MIPRO.2015.7160258>

### **Reconfigurable systems in engineering education : best practices and future trends**

Skliarova, Iuliia; Sklyarov, Valery; **Sudnitsõn, Aleksander; Kruus, Margus** Proceedings of 2017 IEEE Global Engineering Education Conference (EDUCON) : 25-28 April 2017, Athens, Greece 2017 / p. 1084-1088 : ill <https://doi.org/10.1109/EDUCON.2017.7942983>

### **Teaching FPGA-based systems**

Skliarova, Iuliia; Sklyarov, Valery; **Sudnitsõn, Aleksander; Kruus, Margus** 2014 IEEE Global Engineering Education Conference (EDUCON) : Istanbul, Turkey, April 2-5, 2014 2014 / p. 460-469 : ill