

**Architectural design space exploration of an FPGA-based compressed sampling engine : application to wireless heart-rate monitoring**

El-Sayed, Mohammad; Koch, Peter; **Le Moullec, Yannick** 2015 Nordic Circuits and Systems Conference (NORCAS) : NORCHIP & International Symposium on System-on-Chip (SoC) : 1st IEEE NORCAS Conference : 26-28 October 2015, Oslo, Norway 2015 / [5] p. : ill <http://dx.doi.org/10.1109/NORCHIP.2015.7364369>

**Shared Structurally Synthesized BDDs for speeding-up parallel pattern simulation in digital circuits**

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