

Automated design and diagnosis of electronic system based on description language representation

Wawryn, Krzysztof; Drabarek, Jozef BEC : Baltic Electronics Conference : proceedings of the 4th Biennial Conference, October 9-14, 1994, Tallinn (Estonia). 2 1994 / p. 443-448 https://www.ester.ee/record=b2150914*est

Clock manipulation for heterogeneous emulation environment

Ellervee, Peeter; Arhipov, Anton; Tammemäe, Kalle Proceedings [of] 24th IEEE Norchip Conference : Linköping, Sweden, 20-21 November 2006 2006 / p. 213-216 : ill <https://ieeexplore.ieee.org/abstract/document/4126984>

A comparison of six languages for system level description of telecom applications

Jantsch, Axel; Kumar, Shashi; Sander, Ingo; Svantesson, Bengt; Öberg, Johnny; Hemani, Ahmed; Ellervee, Peeter; O'Nils, Mattias Electronic chips & systems design languages 2001 / p. 181-192 : ill https://link.springer.com/chapter/10.1007/978-1-4757-3326-6_15

Comprehensive abstraction of VHDL RTL cores to ESL SystemC = Register-siirde taseme VHDL kirjelduste kompleksne abstraherimine süsteemitaseme SystemC mudeliteks

Abrar, Syed Saif 2016 http://www.ester.ee/record=b4564850*est

Experience in increase of practical hours for HDL course

Reinsalu, Uljana; Ellervee, Peeter 2011 International Conference on Microelectronic Systems Education (MSE '11), 5-6 June 2011, San Diego, California 2011 / p. 102-105 <https://ieeexplore.ieee.org/document/5937104>

A generic scheme for communication representation and mapping

Meinke, Thomas; Jantsch, Axel; Ellervee, Peeter; Hemani, Ahmed; Tenhunen, Hannu 17th NORCHIP Conference : Oslo, Norway, 8-9 November 1999 : proceedings 1999 / p. 334-339 : ill https://www.researchgate.net/publication/2598862_A_Generic_Scheme_for_Communication_Representation_and_Mapping

Handbook of research on emerging rule-based languages and technologies : open solutions and approaches

Giurca, Adrian; Gašević, Dragan; Taveter, Kuldar 2009 https://www.ester.ee/record=b2510538*est

Handbook of research on emerging rule-based languages and technologies : open solutions and approaches

Giurca, Adrian; Gašević, Dragan; Taveter, Kuldar 2009 https://www.ester.ee/record=b2510538*est

HDL-s for students with different background

Reinsalu, Uljana; Arhipov, Anton; Evartson, Teet; Ellervee, Peeter Proceedings MSE 2007 : 2007 IEEE International Conference on Microelectronic Systems Education : 3-4 June 2007, San Diego, CA 2007 / p. 69-70 <https://ieeexplore.ieee.org/document/4231454>

High-level decision diagram simulation for diagnosis and soft-error analysis

Raik, Jaan; Repinski, Urmas; Jenihhin, Maksim; Chepurov, Anton Design and test technology for dependable systems-on-chip 2011 / p. 294-309 : ill <https://www.igi-global.com/chapter/high-level-decision-diagram-simulation/51406>

Lab Description Language - a framework approach for describing and mediating remote and virtual labs

Seiler, Sven; Sell, Raivo; Ptasik, Dennis Mechatronics REM 2012 : Supmeca Paris, November 21st-23rd : proceedings 2012 / p. 194-198 : ill https://www.researchgate.net/publication/256116663_Lab_Description_Language_-_A_framework_approach_for_describing_and_mediating_remote_and_virtual_labs

Model synthesis from VHDL for the functional test generation system

Krupnova, Helena 1993 https://www.ester.ee/record=b2090509*est

On structure of games

Henno, Jaak EJC 2009 : proceedings of the 19th European-Japanese Conference on Information Modelling and Knowledge Bases : Maribor, Slovenia, June 1-5, 2009 2009 / ? p https://www.researchgate.net/publication/221013911_On_Structure_of_Games

Patterns as reusable fragments of knowledge

Eessaar, Erki Databases and information systems : proceedings of the Fifth International Baltic Conference : Baltic DB & IS 2002 : Tallinn, June 3-6, 2002. Vol. 2 2002 / p. 243-248

Riistvara kirjeldamise keel VHDL

Tammemäe, Kalle Arvutustehnika ja Andmetöötlus 1992 / 4, lk. 1-11: ill

Riistvara kirjeldamiskeel - VHDL : metoodiline materjal

1992 https://www.ester.ee/record=b1062926*est

Riistvara kirjeldamiskeel - VHDL : metoodiline materjal

Tammemäe, Kalle 2003 http://www.ester.ee/record=b1605950*est

Riistvara kirjeldamiskeel VHDL : metoodiline materjal

Tammemäe, Kalle 2002 http://www.ester.ee/record=b1605950*est

Riistvara kirjelduskeel Verilog

Ellervee, Peeter A & A 1998 / 1, lk. 6-10

Specification model and language HSL for digital systems

Frištacky, Norbert; Kacerik, Jozef BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 133-136

Translating behavioral VHDL for emulation

Ellervee, Peeter; Reinsalu, Uljana; Arhipov, Anton 25th IEEE NORCHIP Conference : Aalborg, Denmark, 19-20 November 2007 2007 / ? p <https://ieeexplore.ieee.org/document/4481073>

VLSI kõrgtaseme süntees

Tammemäe, Kalle Arvutustehnika ja Andmetöötlus 1993 / 1, lk. 25-31

Язык описания задач УТОПИСТ

Männisalu, Mati; Tõugu, Enn Управляющие системы и машины : УСИМ : научно-производственный журнал 1974 / с. 80-84 : ил https://www.ester.ee/record=b2157161*est