

#### **An ad-hoc implementation of a remote laboratory**

**Azad, Siavoosh Payandeh; Kinks, Hannes; Tajammul, Muhammad Adeel; Ellervee, Peeter** 2015 International Conference on Microelectronic Systems Education : MSE '15 : Pittsburgh, PA, May 20-21, 2015 2015 / p. 48-51 : ill <http://dx.doi.org/10.1109/MSE.2015.7160015>

#### **Digital system modeling and synthesis as an introduction to computer systems engineering**

**Tajammul, Muhammad Adeel; Azad, Siavoosh Payandeh; Ellervee, Peeter** 2015 International Conference on Microelectronic Systems Education : MSE '15 : Pittsburgh, PA, May 20-21, 2015 2015 / p. 52-55 : ill <http://dx.doi.org/10.1109/MSE.2015.7160016>

#### **DyMeP : an infrastructure to support dynamic memory binding for runtime mapping in CGRAs**

**Tajammul, Muhammad Adeel; Jafri, Syed Mohammad Asad Hassan; Ellervee, Peeter; Hemani, Ahmed; Tenhunen, Hannu; Plosila, Juha** 28th International Conference on VLSI Design : held concurrently with 14th International Conference on Embedded Systems : 3-7 January 2015, Bangalore, India : proceedings 2015 / p. 547-552 : ill <http://dx.doi.org/10.1109/VLSID.2015.98>

#### **Polymorphic configuration architecture for CGRAs**

**Jafri, Syed Mohammad Asad Hassan; Tajammul, Muhammad Adeel; Hermani, Ahmed; Paul, Kolin; Plosila, Juha; Ellervee, Peeter; Tenhunen, Hannu** IEEE transactions on Very Large Scale Integration (VLSI) Systems 2016 / p. 403-407 : ill <http://dx.doi.org/10.1109/TVLSI.2015.2402392>

#### **TransMem : a memory architecture to support dynamic remapping and parallelism in low power high performance CGRAs**

**Tajammul, Muhammad Adeel; Jafri, Syed Mohammad Asad Hassan; Hemani, Ahmed; Ellervee, Peeter** 2016 26th International Workshop on Power and Timing Modeling, Optimization and Simulation : PATMOS 2016 : September, 21st to 23th 2016, Bremen, Germany : proceedings 2016 / p. 92-99 : ill <https://doi.org/10.1109/PATMOS.2016.7833431>