

Calculation of probabilistic testability measures for digital circuits with Structurally Synthesized BDDs

Jürimägi, Lembit; Ubar, Raimund-Johannes; Jenihhin, Maksim; Raik, Jaan Microprocessors and microsystems 2020 / art. 103117, 12 p <https://doi.org/10.1016/j.micpro.2020.103117> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

Conditional fault collapsing in digital circuits with shared structurally synthesized BDDs [Online resource]

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Equivalent transformations of structurally synthesized BDDs and applications

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Overview about low-level and high-level decision diagrams for diagnostic modeling of digital systems

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Structurally synthesized multiple input BDDs for simulation of digital circuits

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True path tracing in structurally synthesized BDDs for testability analysis of digital circuits

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