

## **Advanced Hardware Protection Mechanisms : A Study on Logic Locking and Circuit Obfuscation Techniques**

[Võrguteavik] = Täiustatud riistvara kaitsemehhanismid : uuring loogikalukustamise ja hägustamise tehnikate kohta  
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## **An overview of FPGA-inspired obfuscation techniques**

**Abideen, Zain Ul; Gokulanathan, Sumathi; Aljafar, Muayad J.; Pagliarini, Samuel Nascimento** ACM computing surveys 2024 / art. 299, 35 p. : ill <https://doi.org/10.1145/3677118> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)

## **An area aware accelerator for elliptic curve point multiplication**

**Imran, Malik; Pagliarini, Samuel Nascimento;** Rashid, Muhammad Haroon 27th IEEE International Conference on Electronics Circuits and Systems, (ICECS) 2020, Glasgow, UK, Virtual Conference, November 23-25, 2020 : proceedings 2020 / 4 p <https://doi.org/10.1109/ICECS49266.2020.9294908>

## **Benchmarking advanced security closure of physical layouts**

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