

An area aware accelerator for elliptic curve point multiplication

Imran, Malik; Pagliarini, Samuel Nascimento; Rashid, Muhammad Haroon 27th IEEE International Conference on Electronics Circuits and Systems, (ICECS) 2020, Glasgow, UK, Virtual Conference, November 23-25, 2020 : proceedings 2020 / 4 p <https://doi.org/10.1109/ICECS49266.2020.9294908>

Areeba : an area efficient binary huff-curve architecture

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Design space exploration of SABER in 65nm ASIC

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Elliptic-curve crypto processor for RFID applications

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An experimental study of building blocks of lattice-based NIST post-quantum cryptographic algorithms

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A 4-Stage pipelined architecture for point multiplication of binary huff curves

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High-speed design of postquantum cryptography with optimized hashing and multiplication

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High-speed SABER key encapsulation mechanism in 65nm CMOS

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Multiplierless design of high-speed very large constant multiplications

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Multiplierless design of very large constant multiplications in cryptography

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An open-source library of large integer polynomial multipliers

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