

Abituriendid võistlesid programmeerimisoskuses : [17. märtsil TTÜ infotehnoloogia teaduskonna lahtiste uste päeval toimus tarkvara koostamise konkurss-seminar abiturientidele : ka võitjate loetelu]

Kruus, Margus Mente et Manu 2003 / 1. apr., lk. 2 : ill https://www.ester.ee/record=b1242496*est

Address-based data processing over N-ary trees

Sklyarov, Valery; Skliarova, Ioulia; Kruus, Margus; Mihhailov, Dmitri; Sudnitsõn, Aleksander EuroCon 2013 : 01-04 July 2013, Zagreb, Croatia 2013 / p. 1790-1797 : ill

Advanced topics of FSM design using FPGA educational boards and web-based tools

Sudnitsõn, Aleksander; Mihhailov, Dmitri; Kruus, Margus East-West Design & Test Symposium : Moscow, September 18-21, 2009 2009 / p. 446-449 <https://ieeexplore.ieee.org/stamp/stamp.jsp?arnumber=5742086>

An analytical approach to low-power gated-clock finite-state machine solutions

Liiver, Aimar; Tammemäe, Kalle; Kruus, Margus The 7th Biennial Conference on Electronics and Microsystem Technology "Baltic Electronics Conference" : BEC 2000 : October 8 - 11, 2000, Tallinn, Estonia : conference proceedings 2000 / p. 67-70 : ill

Applets for learning digital design and test [Electronic resource]

Ubar, Raimund-Johannes; Jutman, Artur; Kruus, Margus; Wuttke, Heinz-Dietrich 1st International Conference on Interactive Mobile and Computer Aided Learning (IMCL2006) : Amman, Jordan, April 19-21, 2006 2006 / p. 1-4 : ill. [CD-ROM]

Automatic FSM synthesis for low-power mixed synchronous/asynchronous implementation

Oelmann, Bengt; Tammemäe, Kalle; Kruus, Margus; O'Nils, Mattias VLSI design 2001 / 2, p. 167-186 : ill

A benchmark suite for evaluating the efficiency of test tools

Kruus, Helena; Ubar, Raimund-Johannes; Ellervee, Peeter; Gorev, Maksim; Pesonen, Vadim; Devadze, Sergei; Orasson, Elmet; Brik, Marina; Min, Mart; Annus, Paul; Kruus, Margus; Meigas, Kalju BEC 2012 : 2012 13th Biennial Baltic Electronics Conference : proceedings of the 13th Biennial Baltic Electronics Conference : October 3-5, 2012, Tallinn, Estonia 2012 / p. 85-88 : ill

Checking sequence design for FSM

Keevallik, Andres; Kruus, Margus; Lensen, Harri Proceedings of the 5th International Conference on Mixed Design of Integrated Circuits and Systems, Lodz, Poland, June 18-20, 1998 1998 / p. 325-330

Cooperation of FPGA-based educational boards and web-based point design tools for research and education

Sudnitsõn, Aleksander; Mihhailov, Dmitri; Kruus, Margus IFIP EduTech'09 International Workshop : Florianopolis-Brazil, October 15-16, 2009 2009 / ? p

A decomposition procedure for register-transfer level power management

Fomina, Jelena; Keevallik, Andres; Kruus, Margus; Sudnitsõn, Aleksander Proceedings of the International Conference on Computer Systems and Technologies (e-Learning) : CompSysTech'2003 : Sofia, Bulgaria, 19-20 June 2003 / p. 1.2-1 - 1.2-6

Decompositional design of testable FSM networks

Keevallik, Andres; Kruus, Margus; Lensen, Harri Proceedings of the 4th International Workshop Mixed Design of Integrated Circuits and Systems : MIXDES'97 : Poznan, Poland, 12-14 June 1997 1997 / p. 431-436

Diagnostic modelling of digital systems with multi-level decision diagrams

Ubar, Raimund-Johannes; Raik, Jaan; Evertson, Teet; Kruus, Margus; Lensen, Harri Proceedings of the 17th IASTED International Conference on Modelling and Simulation : May 24-26, 2006, Montreal, Quebec, Canada 2006 / p. 207-212 : ill

Digital design at microarch[itectural] level based on quality relationship measures

Kruus, Margus; Lensen, Harri; Sudnitsõn, Aleksander Успехи современного естествознания 2004 / 5, приложение 1, Материалы XXXI международной конференции и дискуссионного научного клуба : информационные технологии в науке, образовании, телекоммуникации и бизнесе : IT+SE'2004 : Украина, Крым, Ялта-Гурзуф, 18-27 мая 2004 года, с. 29-30

Digital synthesis tools for education and research

Fomina, Jelena; Ellervee, Peeter; Kruus, Margus; Sudnitsõn, Aleksander; Tammemäe, Kalle Proc. 18th International Conference on Systems for Automation of Engineering and Research (SAER'2004) 2004 / p. 160-164

DILOS - автоматизированная система декомпозиционного синтеза и верификации дискретных управляющих устройств

Kruus, Margus; Keevallik, Andres; Berkman, Boriss; Sudnitsõn, Aleksander Внедрение новых информационных технологий в процесс обучени профессионально-технических учебных заведений стран - членов СЭВ 1989 / с. ?

Discovering logical constructs from Estonian children language = Loogiliste konstruktsioonide avastamine eesti laste keelest

Matsak, Erika 2009 https://www.ester.ee/record=b2557755*est

Diskreetne matemaatika

Lensen, Harri; Kruus, Margus 2002 http://www.ester.ee/record=b1850141*est

Diskreetne matemaatika

Lensen, Harri; Kruus, Margus 2006 http://www.ester.ee/record=b2223858*est

Diskreetne matemaatika

Lensen, Harri; Kruus, Margus 2012 https://www.ester.ee/record=b2855919*est

Distance-learning tools for digital design and test issues

Jutman, Artur; Kruus, Margus; Sudnitsõn, Aleksander; Ubar, Raimund-Johannes IT+SE'2002 : Information Tec[h]nologies in Science, Education, Telecommunication, Business : proceedings = Информационные технологии в науке, образовании, телекоммуникации, бизнесе, Украина, Крым, Ялта-Гурзуф, 20-30 мая 2002 года : труды 2002 / p. 269-272 : ill

EE : Eesti (Estonia)

Ubar, Raimund-Johannes; Rüstern, Ennu; Kruus, Margus Towards the harmonisation of Electrical and Information Engineering Education in Europe : 2003-2004 2003 / p. 67-74 : ill http://www.ester.ee/record=b2300874*est

E-learning tools for teaching the decomposition based digital synthesis

Sudnitsõn, Aleksander; Kruus, Margus EWME 2006 proceedings : 6th International Workshop on Microelectronics Education : 8-9 June, 2006, Stockholm, Sweden 2006 / p. 63-66 https://pld.ttu.ee/~kruus/EWME_06_sudn.pdf

Electronics design and test

Ubar, Raimund-Johannes; Kruus, Margus; Rang, Toomas Public service review : European Union. 13 2007 / p. 52-53

Environment for innovative university research training in the field of digital test

Oyeniran, Adeboye Stephen; Ademilua, Tolulope; Kruus, Margus; Ubar, Raimund-Johannes 2021 30th Annual Conference of the European Association for Education in Electrical and Information Engineering (EAEEIE) 2021 <https://doi.org/10.1109/EAEEIE50507.2021.9531003>

Esikoht tarkvaratööde konkursil läks I voorus jagamisele : [TTÜ infotehnoloogia teaduskonna korraldatud konkursist abiturientidele]

Kruus, Margus; Viies, Vladimir Mente et Manu 2004 / 14. apr., lk. 4 : fot https://www.ester.ee/record=b1242496*est

European thematic network for doctoral education in computing

Smrikarov, A.; Smrikarova, S.; Kruus, Margus; Sudnitsõn, Aleksander Материалы XXXIII международной конференции и IV международной конференции молодых ученых : информационные технологии в науке, образовании, телекоммуникации и бизнесе = Information Technologies in Science, Education, Telecommunication and Business : IT+SAMPE'06 : майская сессия : Украина, Крым, Ялта-Гурзуф, 20-30 мая 2006 г 2006 / p. 339-340

Experimental comparison of different diagnosis algorithms in the BIST environment

Ubar, Raimund-Johannes; Kostin, Sergei; Raik, Jaan; Kruus, Margus Proceedings of the 16th IASTED International Conference on Applied Simulation and Modelling : August 29-31, 2007, Palma de Mallorca, Spain 2007 / p. 271-276 : ill

Four years of System-on-Chip curricula

Kruus, Margus; Ellervee, Peeter EWME 2006 proceedings : 6th International Workshop on Microelectronics Education : 8-9 June, 2006, Stockholm, Sweden 2006 / p. 88-91

FPGA platform based digital design education

Mihhailov, Dmitri; Kruus, Margus; Sudnitsõn, Aleksander Proceedings of the 9th International Conference on Computer Systems and Technologies and Workshop for PhD Students in Computing : CompSysTech'2008. Vol. 374, ACM International Conference Proceeding Series 2008 / p. IV.4-1 - IV.4-6 : ill

FPGA-based time and cost effective Hamming weight comparators for binary vectors

Skljarov, Valery; Skliarova, Iuliia; Sudnitsõn, Aleksander; Kruus, Margus Proceedings : EUROCON 2015 : Salamanca, Spain, 8th-11th September 2015 / p. 328-333 : ill <http://dx.doi.org/10.1109/EUROCON.2015.7313700>

FSM decomposition using shift registers

Kruus, Margus; Lensen, Harri The 7th Biennial Conference on Electronics and Microsystem Technology "Baltic Electronics Conference" : BEC 2000 : October 8 - 11, 2000, Tallinn, Estonia : conference proceedings 2000 / p. 141-144 : ill

FSM decomposition with application to FPGA synthesis

Sudnitsõn, Aleksander; Mihhailov, Dmitri; Kruus, Margus; Tarletski, Konstantin International Conference on Computer Systems and Technologies - CompSysTech'09 : Ruse, Bulgaria 2009 / p. IV.4.1-IV.4.6

Graduate School in Information and Communication Technologies : experiences at Tallinn University of Technology
Jervan, Gert; Kruus, Margus; Rüstern, Ennu Proceedings MSE 2007 : 2007 IEEE International Conference on Microelectronic Systems Education : 3-4 June 2007, San Diego, CA 2007 / p. 25-26

Hardware close programming for freshmen

Kruus, Helena; Brik, Marina; Kruus, Margus; Ruberg, Priit; Viies, Vladimir; Ellervee, Peeter 10th European Workshop on Microelectronics Education : EWME 2014 : May 14-16, 2014, Tallinn, Estonia 2014 / p. 93-96 : ill

Hierarchical identification of untestable faults in sequential circuits

Raik, Jaan; Ubar, Raimund-Johannes; Krivenko, Anna; Kruus, Margus 10th Euromicro Conference on Digital System Design Architectures, Methods and Tools, DSD 2007 : 29-31 August 2007, Lübeck, Germany : proceedings 2007 / p. 668-671 : ill
<http://dx.doi.org/10.1109/DSD.2007.4341539>

"Hullkoer" käis Tehnikaülikoolis : [TTÜs pidas loengu Linux International'i direktor John "Maddog" Hall]

Kruus, Margus; Raik, Jaan Mente et Manu 2002 / lk. 3 : fot https://www.ester.ee/record=b1242496*est

"Hullkoer" käis Tehnikaülikoolis : [TTÜs pidas loengu Linux International'i direktor John "Maddog" Hall]

Kruus, Margus; Raik, Jaan Arvutikasutaja 2002 / lk. 5 : fot

IKT haridus ülikoolides läbi TTÜ arvutitehnika instituudi vaateprisma

Kruus, Margus; Jervan, Gert Arvutimaailm 2010 / 5, lk. 8-9 : fot https://artiklid.elnet.ee/record=b2409375*est

Improved VHDL input for high-level synthesis tool xTractor

Ellervee, Peeter; Ivask, Eero; Kruus, Margus BEC 2006 : 2006 International Baltic Electronics Conference : Tallinn University of Technology, October 2-4, 2006, Tallinn, Estonia : proceedings of the 10th Biennial Baltic Electronics Conference 2006 / p. 87-90 : ill

Informational modelling of FSM networks

Keevallik, Andres; Kruus, Margus; Udre, Jüri Proceedings of the 4th International Workshop Mixed Design of Integrated Circuits and Systems : MIXDES'97 : Poznan, Poland, 12-14 June 1997 / p. 167-172

Infotehnoloogia teaduskond 2001

Kruus, Margus Tallinna Tehnikaülikooli aastaraamat 2001 2003 / lk. 37-42

Infotehnoloogia teaduskond valis uue dekaani : [Ennu Rüsterni]

Kruus, Margus Mente et Manu 2003 / lk. 2 : portr https://www.ester.ee/record=b1242496*est

Innovation and entrepreneurship in the computer systems curricula and Nordic Master School in innovative ICT

Jervan, Gert; Ellervee, Peeter; Kruus, Margus 22nd EAAEIE annual conference : June, 13-15, 2011, Maribor, Slovenija : conference book 2011 / p. 9

Integration of high-level synthesis to the courses on reconfigurable digital systems

Sklyarov, Valery; Skliarova, Iuliia; Sudnitsõn, Aleksander; Kruus, Margus 2015 38th International Convention on Information and Communication Technology, Electronics and Microelectronics (MIPRO) : May 25-29, 2015, Opatija, Croatia : proceedings 2015 / p. 166-171 : ill <http://dx.doi.org/10.1109/MIPRO.2015.7160258>

Involving students in teaching process — encouraging student-generated content in ICT studies

Kruus, Helena; Ellervee, Peeter; Robal, Tarmo; Ruberg, Priit; Kruus, Margus Proceedings of the 24th International Conference on European Association for Education in Electrical and Information Engineering : 30-31 May 2013, Chania, Greece 2013 / p. 76-81 : ill

Learning digital test and diagnostics via internet

Ubar, Raimund-Johannes; Jutman, Artur; Kruus, Margus; Orasson, Elmet; Devadze, Sergei Wuttke, Heinz-Dietrich International journal of online engineering 2007 / 1, [9] p. : ill https://www.db-thueringen.de/servlets/MCRFileNodeServlet/dbt_derivate_00032681/iJOE_1681-1221_03_2007_1_361.pdf

Learning digital test and diagnostics via internet [Electronic resource]

Ubar, Raimund-Johannes; Jutman, Artur; Kruus, Margus; Orasson, Elmet; Devadze, Sergei Wuttke, Heinz-Dietrich International journal of computing & information sciences 2006 / 2, p. 86-96 : ill

Linnalegendid õppetegevuse tagasisides õppejõu pilgu läbi

Kruus, Margus Mente et Manu 2013 / lk. 16 https://artiklid.elnet.ee/record=b2607297*est

Loodetavasti ajame ühist asja

Kruus, Margus Mente et Manu 2016 / lk. 4-5 : fot

Margus Kruus: arvutimälestused : [õppimisest ja tööst TTÜs elektronarvutite erialal]

Kruus, Margus Arvutikasutaja 2002 / 10, lk. 38 : fot

Microprogram automation : design for testability

Keevallik, Andres; Kruus, Margus; Lensen, Harri BEC'98 : the 6th Biennial Conference on Electronics and Microsystems Technology, October 7-9, 1998, Tallinn, Estonia : proceedings 1998 / p. 171-174

Mõtteid koostöö võimalikkusest Ida-Lääne piiril

Ubar, Raimund-Johannes; Kruus, Margus Mente et Manu 2003 / 20. okt., lk. 2 : portr https://artiklid.elnet.ee/record=b1415646*est

New technique for hierarchical identification of untestable faults in sequential circuits

Krivenko, Anna; Ubar, Raimund-Johannes; Raik, Jaan; Kruus, Margus Info- ja kommunikatsioonitehnoloogia doktorikooli IKTDK kolmanda aastakonverentsi artiklite kogumik : 25.-26. aprill 2008, Voore külalistemaja 2008 / lk. 155-158 : ill

PLA-based FSM state assignment for testability

Kasirova, Lilia; Keevallik, Andres; Kruus, Margus Proceedings of the International Conference "Computer-Aided Design of Discrete Devices", Minsk, 1995 1995 / p. 50-51

Professional standards and curricula of doctoral education in computer engineering

Kruus, Margus; Viies, Vladimir; Sudnitsõn, Aleksander Труды XXXV юбилейной международной конференции. VI международная конференция молодых ученых : информационные технологии в науке, образовании, телекоммуникации и бизнесе : IT+SAMPE'08 : майская сессия : Украина, Крым, Ялта-Гурзуф, 20-30 мая, 2008 г 2008 / p. 302-303

Project-oriented approach to low-power topics in advanced digital design course

Mihhailov, Dmitri; Sudnitsõn, Aleksander; Kruus, Margus Elektronika ir elektrotehnika = Electronics and electrical engineering 2010 / 6, p. 151-154

Reconfigurable systems in engineering education : best practices and future trends

Skliarova, Iouliia; Sklyarov, Valery; Sudnitsõn, Aleksander; Kruus, Margus Proceedings of 2017 IEEE Global Engineering Education Conference (EDUCON) : 25-28 April 2017, Athens, Greece 2017 / p. 1084-1088 : ill
<https://doi.org/10.1109/EDUCON.2017.7942983>

Recursion and hierarchy in digital design and prototyping : a case study

Mihhailov, Dmitri; Kruus, Margus; Sklyarov, Valery; Skliarova, Iouliia; Sudnitsõn, Aleksander Computer Systems and Technologies : 12th International Conference, CompSysTech'11 : Vienna, Austria, June 16-17, 2011 : proceedings 2011 / p. 45-50 : ill

Research in digital design and test at Tallinn University of Technology

Ubar, Raimund-Johannes; Jervan, Gert; Jutman, Artur; Raik, Jaan; Ellervee, Peeter; Kruus, Margus Radioelectronics & informatics 2008 / p. 4-12 : ill <http://www.ewdtest.com/ri/%E2%84%96-1-40-january-march-2008/>

Research on digital system design and test at Tallinn University of Technology

Ubar, Raimund-Johannes; Ellervee, Peeter; Hollstein, Thomas; Jervan, Gert; Jutman, Artur; Kruus, Margus; Raik, Jaan Research in Estonia : present and future 2011 / p. 184-205 : ill

SoC curricula at Tallinn Technical University

Kruus, Margus; Tammemäe, Kalle; Ellervee, Peeter 19th NORCHIP Conference, Kista, Sweden, 12-13 November 2001 : proceedings 2001 / p. 99-104 : ill

Solving computationally intensive problems in reconfigurable hardware : a case study

Skliarova, Iouliia; Vallejo, Tiago; Sklyarov, Valery; Sudnitsõn, Aleksander; Kruus, Margus Journal of convergence information technology (JCIT) : an international journal 2013 / p. 601-609 : ill

Success story of the Computer Engineering Department at the Tallinn University of Technology in EU projects

Ubar, Raimund-Johannes; Kruus, Margus The parliament magazine : European politics and policy 2006 / p. 33 : ill

Synthesis of hierarchically testable FSM networks

Kasirova, Lilia; Keevallik, Andres; Kruus, Margus BEC : Baltic Electronics Conference : proceedings of the 4th Biennial Conference, October 9-14, 1994, Tallinn (Estonia). 1 1994 / p. 347-352: ill

Synthesis of hierarchically testable FSM networks

Kasirova, Lilia; Keevallik, Andres; Kruus, Margus Proceedings of the 2nd Conference "Mixed Design of VLSI Circuits", Lodz, 1995 1995 / p. 253-259

Synthesis of sequential circuits with dynamic power management

Lensen, Harri; Kruus, Margus; Sudnitsõn, Aleksander Scientific proceedings of Riga Technical University. 7.serija, Telecommunications and electronics 2001 / p. 81-86

Synthesis of sequential circuits with dynamic power management

Lensen, Harri; Kruus, Margus; Sudnitsõn, Aleksander Proc. of 42nd International Scientific Conference of Riga Technical University : RTUCET'01 2001 / p. 81-86

Tarkvaratööde konkurss abiturientidele : [korraldab TTÜ infotehnoloogia teaduskond, ajavahemikus dets. 2002 kuni märts 2003 : konkursi tingimused]

Kruus, Margus; Viies, Vladimir Õpetajate Leht 2002 / lk. 11

Tarkvaratööde konkurss IT-teaduskonnas : [TTÜ infotehnoloogia teaduskond korraldab dets. 2002 - märts 2003 abiturientidele suunatud võistkondliku tarkvaratööde konkursi. Konkursist kutsutud osa võtma ka TTÜ esmakursuslased]

Kruus, Margus; Viies, Vladimir Mente et Manu 2003 / 14. jaan., lk. 6 https://www.ester.ee/record=b1242496*est

A task-oriented design of a biologically inspired underwater robot

Listak, Madis 2007 http://www.ester.ee/record=b2298234*est

Tasuta WiFi leviala laienes

Kruus, Margus Mente et Manu 2003 / 13. mai, lk. 3 https://www.ester.ee/record=b1242496*est

Teaching digital system test

Oyeniran, Adeboye Stephen; Ubar, Raimund-Johannes; Kruus, Margus The 27th EAAEIE Annual Conference : June 7-9, 2017, Grenoble 2017 / [6] p

Teaching FPGA-based systems

Skliarova, Ioulia; Sklyarov, Valery; Sudnitsõn, Aleksander; Kruus, Margus 2014 IEEE Global Engineering Education Conference (EDUCON) : Istanbul, Turkey, April 2-5, 2014 2014 / p. 460-469 : ill

Tere tulemast šnautserite maailma!

Kruus, Jelena; Kruus, Margus Koer 1997 / 3, lk. 24-25, 28

The rational unified process with the "4+1" view model of software architecture - a way for modeling Web applications

Robal, Tarmo; Viies, Vladimir; Kruus, Margus Databases and information systems : proceedings of the Fifth International Baltic Conference : Baltic DB & IS 2002 : Tallinn, June 3-6, 2002. Vol. 2 2002 / p. 119-132

TTÜ arvutitehnika instituut sai 50-aastaseks

Kruus, Margus Mente et Manu 2017 / lk. 38-39 : fot http://www.ttu.ee/public/m/mente-et-manu/MM_01_2017/index.html

TTÜ infotehnoloogia tarkvara konkurss 2003 [kooliõpilastele]

Kruus, Margus Arvutikasutaja 2003 / 4, lk. 5

TTÜ infotehnoloogia teaduskond täna ja homme

Kruus, Margus A & A 2003 / 2, lk. 5-8

TTÜ infotehnoloogia teaduskonna ukсед olid lahti

Kruus, Margus A & A 2003 / 2, lk. 62

Tudeng ise peab olema aktiivne

Kruus, Margus Õpetajate Leht 2012 / lk. 7

Using mobile technology to enhance teaching reconfigurable systems

Skliarova, Ioulia; Sklyarov, Valery; Sudnitsõn, Aleksander; Kruus, Margus Proceedings of 2013 IEEE International Conference on Teaching, Assessment and Learning for Engineering (TALE) 2013 / p. 478-483 : ill

Using weighted graphs for fast architecture exploration

Ellervee, Peeter; Klaar, Tarmo; Kruus, Margus; Tammemäe, Kalle BEC 2002 : proceedings of the 8th Biennial Baltic Electronics Conference : October 6-9, 2002, Tallinn, Estonia 2002 / p. 111-114 : ill

Web based tools for synthesis and testing of digital devices

Devadze, Sergei; Jutman, Artur; Kruus, Margus; Sudnitsõn, Aleksander; Ubar, Raimund-Johannes Proceedings of the International Conference on Computer Systems and Technologies (e-Learning) : CompSysTech'2002, Sofia, Bulgaria, 20-21 June 2002 / p. 1.9-1 - 1.9-6 : ill

Web-based software implementation of finite state machine decomposition for design and education

Devadze, Sergei; Kruus, Margus; Sudnitsõn, Aleksander CompSysTech' 2001 : proceedings of the International Conference on Computer Systems and Technologies, Sofia, 21-22 June 2001 / [6] p. : ill

Web-based software package for e-learning and research training in digital system design

Jutman, Artur; Kruus, Margus; Sudnitsõn, Aleksander; Ubar, Raimund-Johannes; Wuttke, Heinz-Dietrich Информационные технологии в науке, образовании, телекоммуникации и бизнесе : Материалы XXXII Международной конференции IT+SE'2005 : Украина, Крым, Ялта-Гурзуф, 19-28 мая 2005 г 2005 / [2] p

Web-based system for sequential machines decomposition

Devadze, Sergei; Fomina, Jelena; Kruus, Margus; Sudnitsõn, Aleksander The IEEE Region 8 EUROCON 2003 : Computer as a Tool : 22-24. September 2003, Ljubljana, Slovenia : proceedings. Volume A 2003 / p. 57-61 : ill

Web-based tools for decomposition-oriented digital design

Kruus, Margus; Lensen, Harri; Sudnitsõn, Aleksander MEET/MARIND'2002 : proceedings of First International Congress on Mechanical and Electrical Engineering and Technology and Fourth International Conference on Marine Industry, 07-11 October 2002, Varna Bulgaria. Volume 1 2002 / p. 261-266 : ill

Web-based tools for finite state machine decomposition with analysis of information flows

Fomina, Jelena; Keevallik, Andres; Kruus, Margus; Sudnitsõn, Aleksander BEC 2002 : proceedings of the 8th Biennial Baltic Electronics Conference : October 6-9, 2002, Tallinn, Estonia 2002 / p. 165-168 : ill

Virtual European department of computing project : problems and perspectives

Kruus, Margus; Sudnitsõn, Aleksander Материалы XXX юбилейной международной конференции и I международной конференции молодых ученых : Информационные технологии в науке, образовании, телекоммуникации и бизнесе = Information Technologies in Science, Education, Telecommunication and Business : IT + SE'2003 : Украина, Крым, Ялта-Гурзуф, 19-28 мая 2003 года 2003 / p. 280-283

Õppimisvõimalused TTÜ arvutitehnika instituudis

Kruus, Margus Arvutimaailm 1999 / 4, lk. 14-15: fot

Декомпозиционный метод синтеза контролепригодных цифровых автоматов

Kruus, Margus Машинное проектирование электронных устройств и систем 1986 / с. 52-56

Контрольные эксперименты с микропрограммными автоматами

Kruus, Margus Методы синтеза и диагностирования цифровых схем 1985 / с. 45-50

Метод нахождения диагностических и установочных последовательностей для сетей автоматов

Keevallik, Andres; Kruus, Margus Машинное проектирование электронных устройств и систем 1986 / с. 43-51

Методы верификации при декомпозиционном синтезе ДУУ

Kruus, Margus; Keevallik, Andres; Kaširova, Lilia; Salum, Kaja Arvutustehnika kasutamise efektiivsuse tõstmise teed = Пути повышения эффективности использования вычислительной техники : konverentsi ettekande teesid 1989 / lk. 53-54
https://www.ester.ee/record=b1215350*est

О декомпозиции конечных автоматов на базе сдвиговых регистров

Keevallik, Andres; Kruus, Margus; Salum, Kaja Машинное проектирование электронных устройств и систем 1988 / с. 3-11

О реализации микропрограммных автоматов на сдвиговых регистрах

Leis, Paul; Keevallik, Andres; Kruus, Margus Расчет и проектирование систем технической кибернетики 1984 / с. 85-95

Построение контрольной последовательности для управляющих автоматов, реализованных на программируемых логических матриц

Kaširova, Lilia; Kruus, Margus; Ellervee, Peeter Машинное проектирование электронных устройств и систем 1989 / с. 77-88

Построение проверяющего теста для сети автоматов

Keevallik, Andres; Kaširova, Lilia; Kruus, Margus Tallinna Tehnikaülikooli Toimetised 1990 / lk. 3-19: ill

Разработка декомпозиционных методов синтеза контролепригодных дискретных управляющих устройств : автореферат ... кандидата технических наук (05.13.01)

Kruus, Margus 1987 https://www.ester.ee/record=b1282510*est

Разработка декомпозиционных методов синтеза контролепригодных дискретных управляющих устройств : диссертация на соискание ученой степени кандидата технических наук

Kruus, Margus 1987 https://www.ester.ee/record=b4636146*est

Экспериментальное исследование алгоритмов синтеза контрольных последовательностей для управляющих автоматов

Kruus, Margus; Salum, Kaja Машинное проектирование электронных устройств и систем 1989 / с. 33-42

Электротехника и автоматика

Ubar, Raimund-Johannes; Rang, Toomas; Velmre, Enn; Evertson, Teet; Voolaine, A.; Toome, Tõnis; Viilup, Agu; Sudnitsõn, Aleksander; Berkman, Boriss; Rüstern, Ennu; Leis, Paul; Keevallik, Andres; Kruus, Margus; Jüris, A. 1984

https://www.ester.ee/record=b1549179*est

Электротехника и автоматика

Pikkov, Otto; Keevallik, Andres; Lausmaa, Toomas; Sudnitsõn, Aleksander; Leis, Paul; Ellervee, Peeter; Berkman, Boriss; Alango, Villem; Kitsnik, Peeter; Kont, Toomas; Kruus, Margus; Ubar, Raimund-Johannes; Evertson, Teet; Lohuaru, Tõnu; Räisa, O.; Toome, Tõnis; Šendrik, M.G.; Tamm, Boris, inform. 1985 https://www.ester.ee/record=b1356648*est

Электротехника и автоматика

Rüstern, Ennu; Leis, Paul; Sudnitsõn, Aleksander; Viies, Vladimir; Berkman, Boriss; Keevallik, Andres; Kruus, Margus; Ubar, Raimund-Johannes; Alango, Villem; Kont, Toomas; Grigorjeva, Ksenja; Einasto, N.; Evertson, Teet; Gerasimtsuk, Valeri; Mahhitko, V.P.; Šendrik, M.G.; Tamm, Boris, inform. 1986 https://www.ester.ee/record=b1296477*est

Электротехника и автоматика

Rüstern, Ennu; Keevallik, Andres; Kruus, Margus; Salum, Kaja; Berkman, Boriss; Tammemäe, Kalle; Alango, Villem; Kont, Toomas; Ubar, Raimund-Johannes; Lohuaru, Tõnu; Štraube, B.; Elst, G.; Bombik, B.; Viies, Vladimir; Gallai, S.; Rang, Toomas; Laansoo, Ants; Männama, Vello; Pikkov, Otto; Gurjanov, Boris; Opotski, Aleksei; Velmre, Enn 1988

https://www.ester.ee/record=b1256708*est

Электротехника и автоматика

Mägi, Harri; Velmre, Enn; Pikkov, Mihhail; Orro, S.; Rang, Toomas; Gurjanov, Boris; Kruus, Margus; Salum, Kaja; Berkman, Boriss; Keevallik, Andres; Kasirova, Lilia; Kruus, Margus; Ellervee, Peeter; Ubar, Raimund-Johannes; Grigorjeva, Ksenja; Kont, Toomas 1989 https://www.ester.ee/record=b1285446*est