

An overview of FPGA-inspired obfuscation techniques

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Leveraging layout-based effects for locking analog ICs

Aljafar, Muayad J.; Azais, Florence; Flottes, Marie-Lise; Pagliarini, Samuel Nascimento ASHES'22: Proceedings of the 2022 Workshop on Attacks and Solutions in Hardware Security 2022 / p. 5-13 <https://doi.org/10.1145/3560834.3563826>

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SCALLER: standard cell assembled and local layout effect-based ring oscillators

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Survey on the benefits of using memristors for PUFs

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