

### **Leveraging layout-based effects for locking analog ICs**

**Aljafar, Muayad J.**; Azais, Florence; Flottes, Marie-Lise; **Pagliarini, Samuel Nascimento** ASHES'22: Proceedings of the 2022 Workshop on Attacks and Solutions in Hardware Security 2022 / p. 5-13 <https://doi.org/10.1145/3560834.3563826>

### **Utilizing layout effects for analog logic locking**

**Aljafar, Muayad J.**; Azais, Florence; Flottes, Marie-Lise; Pagliarini, Samuel Journal of Cryptographic Engineering 2024 / p. 311-324 <https://doi.org/10.1007/s13389-024-00350-8> [Journal metrics at Scopus](#) [Article at Scopus](#) [Journal metrics at WOS](#) [Article at WOS](#)