

Architectural solutions for high-speed data processing demands of CERN LHC detectors with FPGA and high-level synthesis

Devadze, Sergei; Nielsen, Christine Elizabeth; **Mihhailov, Dmitri**; **Ellervee, Peeter** 2024 IEEE Nordic Circuits and Systems Conference (NorCAS) 2024 <https://doi.org/10.1109/NorCAS64408.2024.10752490> [Article at Scopus](#)

Ten questions concerning environmental architectural design exploration

De Luca, Francesco; Natanian, Jonathan; Wortmann, Thomas Building and environment 2024 / art. 111697
<https://doi.org/10.1016/j.buildenv.2024.111697>