A choise of flexible, ultraclean processing strategy for advanced minifabs of competitive ULSI

Bubennikov, Alexander N.; Bubennikov, Alexander A. BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 125-128: ill

Compact avalanche transistor model for circuit simulation of Bi and BiCMOS cells

Bubennikov, Alexander N.; Kobosev, Gennady; Kozhuhov, D.; Uchenov, A. BEC : Baltic Electronics Conference : proceedings of the 4th Biennial Conference, October 9-14, 1994, Tallinn (Estonia). 2 1994 / p. 523-527: ill

Complex simulation of punchthrough, tunneling and high-speed characteristics of scaled bipolar devices and circuits

Bubennikov, Alexander N.; Ishevsky, D.V. Automation, simulation & measurement : ASM'91 : 3rd biennal conference, Tallinn, October 7-11, 1991. Section S / Tallinn Technical University 1992 / p. 26-31: ill

Quasistatic methods for measuring the emitter, collector and base resistances in advanced bipolar transistors

Bubennikov, Alexander N.; Kobosev, Gennady BEC'96 : the 5th Biennial Baltic Electronics Conference, October 7-11, 1996, Tallinn, Estonia : proceedings 1996 / p. 117-120: ill

TCAD with device-circuit simulators for advanced process development, correlations between model parameters and optimization

Bubennikov, Alexander N. BEC : Baltic Electronics Conference : proceedings of the 4th Biennial Conference, October 9-14, 1994, Tallinn (Estonia). 2 1994 / p. 517-522: ill

Technonomics of manufacturing of silicon microsystems on WSI

Bubennikov, Alexander N.; Bubennikov, Alexander A. BEC'98 : the 6th Biennial Conference on Electronics and Microsystems Technology, October 7-9, 1998, Tallinn, Estonia : proceedings 1998 / p. 29-32: ill