

**Combining symbolic techniques with topological approach in test generation**

**Ubar, Raimund-Johannes** Proceedings of the 3rd Workshop on Mixed Design of Integrated Circuits and Systems, Lodz, May 1996  
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**Teaching test and design for testability with TURBO-TESTER software**

**Jervan, Gert; Markus, Antti; Paomets, Priidu; Raik, Jaan; Ubar, Raimund-Johannes** Proceedings of the 3rd Workshop on  
Mixed Design of Integrated Circuits and Systems, Lodz, May 1996 1996 / p. 589-594